### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Examiner: David Graybill Group Art Unit: 2827

July 30, 2003

	APPLICATION of: Mitsuru KOMIYAMA and Shinsuke SUZUKI	)
Appl No. :	09/963,590	) ) NINFORMATION
Filed :	September 27, 2001	) INFORMATION ( ) DISCLOSURE ( ) STATEMENT
For :	MULTI-CHIP PACKAGE TYPE SEMICONDUCTOR DEVICE	) <u>Statement</u> ) )
Attorney Ref.:	F00ED0023	) )

Commissioner of Patents P.O. BOx 1450 Alexandria, Va 22313-1450

Sir:

This is an Information Disclosure Statement submitted in compliance with the timing requirements of 37 C.F.R. § 1.97(c)(2).

Attached are copies of (1) one Office Action from Japanese Patent and Trademark Office and its English translation, (2) two Japanese laid open patent publications and their English abstracts, (3) one US patent publication, which is a counterpart of one Japanese laid open patent publication mentioned above, and (4) one International Application Publication. The publications are listed on the attached Form PTO-1449.

Since this Information Disclosure Statement is being filed based on 37 C.F.R. § 1.97(c)(2) with the fee set forth in § 1.17(p), no statement specified in paragraph (e) of 37 C.F.R. § 1.97 is required, and the requirements of 37 C.F.R. §§ 1.97 and 1.98 are deemed to be fully met as to all documents submitted. Consideration of the submitted documents is respectfully requested.

07/31/2003 MGEBREM1 00000096 500945 09963590

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# Respectfully submitted,

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July 30, 2003



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<b>INFORMATION</b>	<b>DISCLOSURE</b>	<b>STATEMENT</b>

F00ED0023

U.S. Application No.: 09/963,590

**Applicant** 

Mitsuru KOMIYAMA and Shinsuke SUZUKI

U.S. Filing Date: September 27, 2001

Group 2827

#### **U.S. PATENT DOCUMENTS**

Examiner Initial		Document Number	Date	Name	Class	Sub- Class	Filing Date
	AA	US2002/0158325 A1	10/31/02	Yano et al.	257	686	06/06/02
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	АН						
	AI						

#### **FOREIGN PATENT DOCUMENTS**

FOREIGN FATENT BOCOMENTO						
	Document Number	Date	Country	Class	Sub- Class	Translation
AL	Japanese Laid open patent 2001-196529	07/19/01	JAPAN	H01L	25/065	Abstract
AN	Japanese Laid open patent 2000-307057	11/02/00	JAPAN	H01L	25/065	Abstract
AN	International Publication No. WO 97/25742	07/17/97	WIPO	H01L	23/48	
AC						

## OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

AR	
AS	

Examiner

**Date Considered** 

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.